

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/700,391	§	Examiner:	Johnson, Brian P.
Filed:	November 4, 2003	§	Group/Art Unit:	2183
Inventor(s):		§	Atty. Dkt. No:	5500-91300
Arun Radhakrishnan and Kelvin		§		
D. Goveas		§		
		§		
		§		
Title:	Prediction of Floating Point	§		
	Instruction Latency and	§		
	Scheduling Based on	§		
	Prediction	§		
		§		
		§		
		§		

**RESPONSE TO OFFICE ACTION OF
October 17, 2006**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of October 17, 2006, to further highlight why the application is in condition for allowance.